



**Series
TFI243-500**

High Frequency Inverter grade Capsule Thyristor Type TFI243-500

Low switching losses

Low reverse recovery charge

Distributed amplified gate for high di/dt

Maximum mean on-state current	ITAV 500 A							
Maximum repetitive peak off-state and reverse voltage	UDRM 800 ÷ 1500 V							
Turn-off time	tq 12,5; 16; 20; 25 µs							
UDRM, URRM, V	800	900	1000	1100	1200	1300	1400	1500
Voltage code	8	9	10	11	12	13	14	15
Tvj, °C	- 60 ÷ 125							

MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	TFI243-500	Conditions
ITAV	Mean on-state current	A	500 830	Tc=90 °C, Tc=55 °C, 180° half-sine wave, 50 Hz
ITRMS	RMS on-state current	A	785	Tc=90 °C
ITSM	Surge on-state current	kA	10,0 11,0	Tvj=125°C Tvj=25°C
I ² t	Limiting load integral	kA ² s	500 605	Tvj=125°C Tvj=25°C
UDRM, URRM	Repetitive peak off-state and reverse voltage	V	800÷1500	Tj min≤Tvj≤TjM 180° half-sine wave, 50 Hz Gate open
UDSM, URSM	Non-repetitive peak off-state and reverse voltage	V	880÷1600	Tj min≤Tvj≤TjM 180° half-sine wave tp=10 ms, Single pulse Gate open
(di _T /dt) crit	Critical rate of rise of on-state current : non - repetitive repetitive	A/µs	2000 1250	Tvj=125°C ; UD=0,67 UDRM, Gate pulse : 10V, 5 Ω, 1µs rise time, 10 µs
URGM	Peak reverse gate voltage	V	5	Tj min≤Tvj≤TjM
Tstg	Storage temperature	°C	-60÷80	
Tvj	Junction temperature	°C	-60÷125	

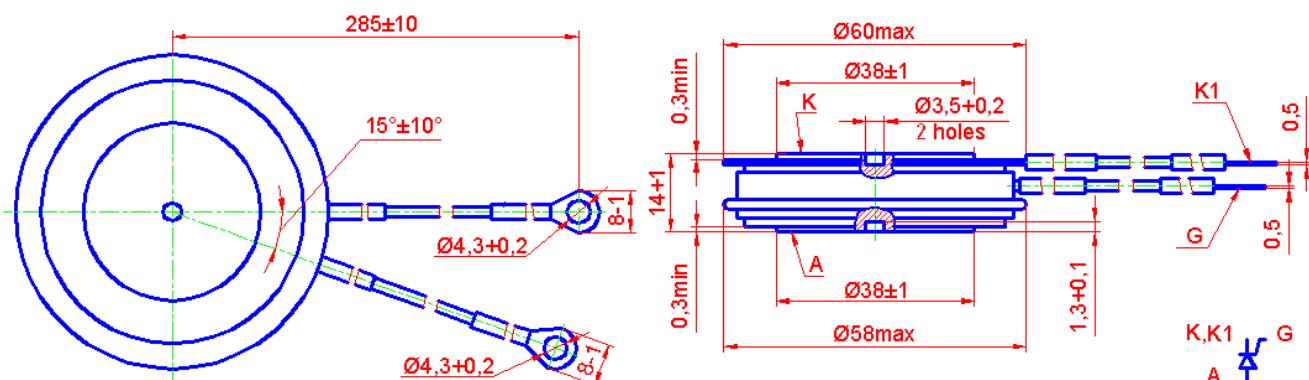
CHARACTERISTICS

UTM	Peak on-state voltage	V	2,4	Tvj=25°C, ITM=3,14 ITAV
UT(TO)	Threshold voltage	V	1,4	Tvj=125°C
RT	On-state slope resistance	mΩ	0,6	1,57 ITAV < IT < 4,71 ITAV
IDRM IRRM	Repetitive peak off-state and reverse current	mA	50 50	Tvj=125°C, UD = UDRM UR= URRM

CHARACTERISTICS						
Symbols and parameters		Units	TFI243-500	Conditions		
I _L	Latching current		A	7	Tvj=25°C, UD=12V Gate pulse : 10V, 5Ω, 1 µs rise time, 10µs	
I _H	Holding current		A	0,5	Tvj=25°C, UD=12V, Gate open	
UGT	Gate trigger direct voltage		V	2,5 5,0	Tvj=25°C, Tvj=-60°C	UD=12V
IGT	Gate trigger direct current		A	0,3 0,85	Tvj=25°C, Tvj=-60°C	
UGD	Gate non-trigger direct voltage		V	0,25	Tvj=125°C, UD = 0,67 UDRM	
IGD	Gate non-trigger direct current		mA	10	Direct gate current	
t _{gd}	Delay time		µs	1,6	Tvj=25°C, UD=500V ITM = 500 A	
t _{gt}	Turn-on time		µs	2,5	Gate pulse : 10V, 5Ω, 1 µs rise time, 10µs	
t _q	Turn-off time		µs	12,5÷25 16÷32	Tvj=125°C, ITM =500 A di _R /dt =10 A/µs, UR=100V UD = 0,67 UDRM du _D /dt=50 V/µs du _D /dt=200 V/µs	
Qrr	Recovered charge		µC	230		
trr	Reverse recovery time		µs	4,0	Tvj=125°C, ITM =500 A	
Irrm	Peak reverse recovery current		A	115	dir/dt =50 A/µs, UR=100V	
(dud/dt)crit	Critical rate of rise of off-state voltage		V/µs	500 1000	Tvj=125°C, UD = 0,67 UDRM Gate open	
Rthjc	Thermal resistance junction to case		°C/W	0,032	Direct current, double side cooled	

ORDERING							
	TFI	243	500	14	7	7	3
	1	2	3	4	5	6	7

- Fast thyristor with interdigitated gate structure.
- Design version.
- Mean on-state current, A.
- Voltage code (14=1400 V).
- Critical rate of rise of off-state voltage ($6 \geq 500 \text{ V/}\mu\text{s}$, $7 \geq 1000 \text{ V/}\mu\text{s}$).
- Group of turn-off time ($\text{du}_D/\text{dt}=50 \text{ V/}\mu\text{s}$, $5 \leq 25\mu\text{s}$, $6 \leq 20\mu\text{s}$, $7 \leq 16\mu\text{s}$, $8 \leq 12,5\mu\text{s}$).
- Group of turn-on time ($3 \leq 2,5\mu\text{s}$).



Mounting force : 13÷19 kN
Weight : 210 grams